

Venkata Vish Ganti

100 Font Blvd, Apt 4J. San Francisco, CA 94132

gvishwanth@gmail.com / (415)-260-4196

<p><u>Professional Summary</u></p>	<p>Masters in Engineering with specialization in Electrical Engineering. 1.5 years of experience as an electrical engineer in energy industry. I excel in challenging engineering roles and in environments that provide me the opportunity to learn and grow. My key competencies include I am goal oriented, with excellent communication skills, tenacity and the ability to successfully complete complex tasks with minimal supervision</p>												
<p><u>Professional Experience</u></p>	<table border="1"> <tr> <td data-bbox="284 422 1096 489"> <p>Electrical Engineer Industrial Assessment Center (IAC): San Francisco State University</p> </td> <td data-bbox="1096 422 1507 489"> <p>March 2009-Present</p> </td> </tr> <tr> <td colspan="2" data-bbox="284 489 1507 1073"> <p>Accomplishments:</p> <ul style="list-style-type: none"> • Audited 20 manufacturing industries in Northern California • Lead Electrical Engineer for the Industrial Assessment Team for 6 months • Developed energy conservation measures and recommendations for high efficiency Lighting, lighting controls, high efficiency motors and pumps, solar PV system sizing, power factor correction and rate schedule change. • Extensive exposure towards using Onset Hobo and Dent Data loggers, Power Meter, Gas Analyzer • Acquired understanding towards various industrial and manufacturing processes. • Good understanding of variable frequency drives pumps, boilers air compressors and HVAC • Designed and maintained website. • Assisted in the recruiting and training of the new team members • Assisted in general administration and smooth operation of the IAC. • Audited Classes at PG&E's Pacific Energy Center, San Francisco <ul style="list-style-type: none"> ➤ California Solar Incentive for PV Systems ➤ Basics of Grid Tied PV System Sizing and Cost Analysis ➤ Audit Skills, Tools, Measurement and Data Analysis </td> </tr> <tr> <td data-bbox="284 1073 1096 1140"> <p>Research Assistant Nano Electronics and Research Computing Laboratory -San Francisco State University</p> </td> <td data-bbox="1096 1073 1507 1140"> <p>Sept 2008-Present</p> </td> </tr> <tr> <td colspan="2" data-bbox="284 1140 1507 1438"> <p>Accomplishments:</p> <ul style="list-style-type: none"> • Research on H-Tree Clock Distribution Network in 32nm technology • Research on aging effects , leakage currents and benefits of using carbon nanotubes for VLSI circuits • Developed Schematic, Layout and StarXct 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<p><u>Other Experience</u></p>	<table border="1"> <tr> <td data-bbox="284 1808 1096 1875"> <p>Technical Support Associate for AT&T Internet Satyam BPO Call Center, India</p> </td> <td data-bbox="1096 1808 1507 1875"> <p>April 2007 – November 2007</p> </td> </tr> <tr> <td data-bbox="284 1875 1096 1942"> <p>Webmaster and Network Administrator PRRM Engineering College, India</p> </td> <td data-bbox="1096 1875 1507 1942"> <p>May 2006 – July 2008</p> </td> </tr> </table>	<p>Technical Support Associate for AT&T Internet Satyam BPO Call Center, India</p>	<p>April 2007 – November 2007</p>	<p>Webmaster and Network Administrator PRRM Engineering College, India</p>	<p>May 2006 – July 2008</p>								
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<u>Education</u>	San Francisco State University(SFSU) , San Francisco, CA	Degree expected July 2010
	Master of Science in Electrical Engineering	GPA: 3.6/4.0
	Jawaharlal Nehru Technological University , Hyderabad, India	May 2008
	Bachelor of Technology in Electronics & Communication Engineering	GPA: 3.2/4.0
<u>Relevant Coursework</u>	Computer Communications and Networks Engineering Communication VLSI Digital Design Advanced VLSI Design Advanced Digital Design	Engineering Management Business Management Renewable Energy Sources Radio Frequency(RF) Analog Circuits Electrical Transmission Line Technology
<u>Projects</u>	<ul style="list-style-type: none"> Applying Six sigma quality control in Engineering Industry Best Project Recognition for designing a 4-bit ripple carry adder in 90nm CMOS technology using Cosmos Schematic & Layout Tools. Dynamic speed control and warning system using Atmel 16 bit-microcontroller Design a motion estimator using Verilog/synthesis techniques. 	
	Master's Project: Comparative Study of Carbon Nano-Tube Interconnects and Copper Interconnects for Global and Intermediate Clock Distribution Networks.	
	Senior Project: Microcontroller driven range estimation and motion control system for humanoid robots Using Ultrasonic and Infrared sensors.	
<u>Publications</u>	"Full-Custom Design Project for Digital VLSI and IC Design Courses using Synopsys Generic 90nm CMOS Library" with Eli Lyons, Dr.H.Mahmoodi. Accepted for IEEE International Conference Microelectronic Systems Education (MSE) 2009 Conference.	
	"CMOS Inverter Design Manual Using Synopsys Cosmos Scope and Cosmos Layout Editor" with Dr.H.Mahmoodi. NeCRL, San Francisco State University. November 2008	
<u>Professional Certifications</u>	Microsoft Certified Systems Engineer(MCSE)	May 2008
	Cisco Certified Network Associate(CCNA)	July 2008
<u>Professional Associations</u>	Member of Association of Energy Engineers (AEE) Member of IEEE , Power & Energy and Communication Societies	
<u>Technical Skills</u>	Programming Languages	C, Assembly Language, Verilog, AVR Studio, VHDL, HTML, Perl, php,
	Operating Systems	Windows XP and Server 2008 , Unix, Linux, Mac
	Applications	Microsoft Word, Excel, PowerPoint, Visio, Dreamweaver, AutoCAD, SolidWorks
	EDA Tools	Pspice and Hspice, Cadence, Synopsys Schematic and Layout Editor ,Multisim
	Energy Related	MotorMaster+, PSAT, FSAT, AirMaster, PvSyst, Onset Hoboware Pro, Meteororm, eQuest, ComCheck
<u>"Soft" Skills</u>	Managers, mentors and references have described me as someone with: <ul style="list-style-type: none"> Excellent written communication and presentation skills An understanding of diversity and the ability to successfully work in diverse cultures and countries Strong interpersonal skills A person with diverse skills ability to provide suggestions and solutions in various domains A team player that also possesses outstanding leadership skills The ability to work under severe pressure and time constraints Intelligent and a quick learner 	